

Use of the MC68HC68T1 RTC with M6805 Microprocessors

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INTRODUCTION

The MC68HC68T1 is a real time clock (RTC) which also contains 20 bytes of RAM. Communication is via a serial port making the T1 suitable for use with single-chip microprocessors. The features on the MC68HC68T1 are similar to those on the MC146818 which, with its multiplexed-bus, is not so appropriate for use with MCUs. The MC68HC68T1 has, in addition, watchdog and power fail capabilities.

The MC68HC68T1 can be controlled using a clocked serial port, typically an SPI, but can also use port lines. This would be of use with a micro with no serial capability or in an application where the serial port was dedicated to another task. The example software was developed for use on an MC68HC05C4 and includes routines to use either the SPI or port lines.

Using one or other of these methods the code could be used in any 6805 microprocessor. The code would need to be modified to exclude the STOP instruction in an HMOS processor.

The example software constitutes an alarm clock using an 8-digit triplexed LCD display with ICM7231B driver, an MC68HC805C4 microprocessor and the RTC as shown in Figure 1. The software leaves the micro in stop mode until it is interrupted by the MC68HC68T1 in order to update the display. This is selected to be 1Hz by the software. The watchdog requirement cannot be met while the micro is in stop mode and is consequently not enabled in the software. As the software is for a battery supplied clock the power fail capability is also not used.

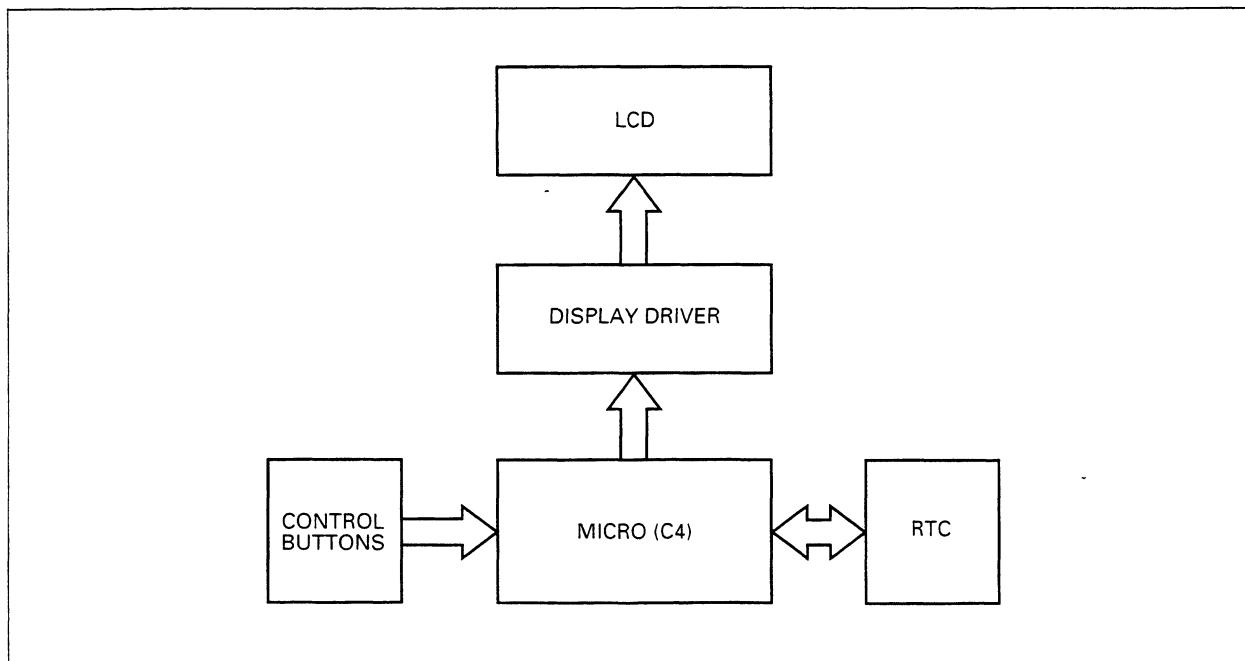


Figure 1.

CIRCUIT

Figure 2 shows the circuit used. The MC68HC68T1 can be connected either to port lines or to the SPI. With the two jumpers on MOSI and SCK connected between 1 & 2 the port lines are used. The third jumper on PD7 should also be connected between 1 & 2 to indicate to the software that I/O port communication is required. Jumper connection between 2 & 3 selects SPI communication. As MISO is an output from the RTC it can be connected in parallel to both the MISO and PC7 inputs on the C4.

The RTC uses a 32KHz clock for low power dissipation. The RTC and display driver consume about 65 microamps at 5.0v while the 2 milliamps taken by the C4 at a duty cycle of less than 1% contributes only another 15 microamps. The crystal frequency used on the C4 does not significantly affect overall consumption, 2MHz was used.

The ICM7231B LCD driver requires each character to be addressed through pins A0, A1 & A2 and the appropriate data written to pins D0, D1, D2, & D3. As the display is written to after the RTC registers have been read the port lines PC0 & PC1 can be used for both interfaces. The annunciators and decimal points (the latter not used in the example software) are controlled by their own pins. The ICM7231B driver used in the clock, displays -, E, H, L, P and blank to represent A, B, C, D, E and F respectively. The "A" version of this driver displays A through F normally but does not display a "-" or a blank and is therefore not suitable. Contrast can be adjusted using the potentiometer on pin 2 of the ICM7231B. With the clock supplied by a battery of four zinc-carbon cells a fixed 100 kohms from pin 2 to ground was found to be satisfactory.

SOFTWARE

The first page of the software listing constitutes hardware address definitions and RAM allocation. As well as working space the RAM contains an image of the registers within the 68HC68T1. These consist of 3 time registers, 4 date registers, 3 alarm registers and 3 status/control registers. The alarm registers are different from the time/date registers in that they are write only and consequently have been treated differently in software.

Interrupts are vectored to address \$100 and the code at the start of the second page decides where the interrupt has come from. If it is not the result of a button press it must be from the MC68HC68T1 and its registers are read. Firstly the status register is read. This serves to return the interrupt line to a high and also allows the alarm bit (bit 1) to be checked to see if an alarm has occurred. If it has then bit 7 on port B is set high and the T1's clock output is enabled at 64 Hz. Either signal can be used to provide an alarm.

All the time registers are then read and displayed according to the selected display format (see below). The second page handles time displays and the third page those of date and alarm. As the alarm registers in the MC68HC68T1 are write only, alarm data is derived from the RAM of the micro.

Button 1 has two functions according to whether the clock is in the normal or the set-up mode. The mode is contained in status bit 0 in RAM location STAT. In set-up mode it increments the display format in RAM location DSPLY. In the set-up mode it increments the register currently "opened" by button 2. In this mode it sets STAT bit 1 to indicate to the main set-up routine that a change has been made. This part of the software also keeps track of the maximum permissible value for each register.

Button 2 is only used for set-up. When pressed it selects the set-up mode and displays the first register (time seconds). Subsequent presses move to the next register. A press from the last register (alarm hours) returns the clock to normal mode. Entry to the set-up routine stops further interrupts from the MC68HC68T1. The time and date registers are read from the MC68HC68T1 and so will be correct if its power has been maintained even if the micro's RAM is not valid (alarm values are read from RAM). If a value is changed, by pressing button 1, then the new value is written to the appropriate MC68HC68T1 register when the next register is selected. Alarm values are written immediately to the MC68HC68T1 so that the displayed value is the same as that in the corresponding MC68HC68T1 alarm register.

The serial routine on page 12 communicates with the MC68HC68T1 using I/O lines and the next page shows how this can be done with less code using the SPI. The software is written for the level 1 SPI contained in the MC68HC05C4 & L6.

ALARM CLOCK OPERATION

The clock is controlled by two buttons. Button 1 selects the format of the display by sequencing through the formats shown in Table 1.

Format 5 shows the day of the week using the annunciator on the display. A "1" in register 6 (\$24) of the MC68HC68T1 which corresponds to Sunday displays at position 2 and so on through Saturday at position 8. This only appears when the display is set to date. Position 1 is reserved to indicate that the alarm is enabled and shows on all display formats if the alarm SPST switch is closed. Button 1 is also used to cancel the alarm.

Button 2 allows the various registers in the MC68HC68T1 to be set up. When this button is pressed the number in position 5 of the display indicates the register whose contents are being displayed in position 7 and 8 (see table 2). Pressing button 2 again moves to the next register without changing anything, pressing button 1 while in this mode increments the contents of the displayed register, the new contents are written into the register when button 2 is used to move to the next register.

Pressing button 2 when the last register (9) is being displayed returns the clock to the normal run mode in which the required format can be selected using button 1.

No.	Display Format
1	Time - hours (24 hr format), minutes and seconds.
2	Time - hours (24 hr format), minutes and date.
3	Time - hours (12 hr format), minutes and seconds.
4	Time - hours (12 hr format), minutes and date.
5	Date - day, month and year including day of week.
6	Alarm time in 24 hr format.

Table 1 Display Formats

Display	Address	Function	Range
0	\$20	seconds	0 - 59
1	\$21	minutes	0 - 59
2	\$22	hours	0 - 23
3	\$23	day of week	1 - 7 (Sun. - Sat.)
4	\$24	day of month	1 - 31
5	\$25	month	1 - 12
6	\$26	year	0 - 99
7	\$28	alarm seconds	0 - 59
8	\$29	alarm minutes	0 - 59
9	\$2A	alarm hours	0 - 23

Table 2 MC68HC68T1 Registers

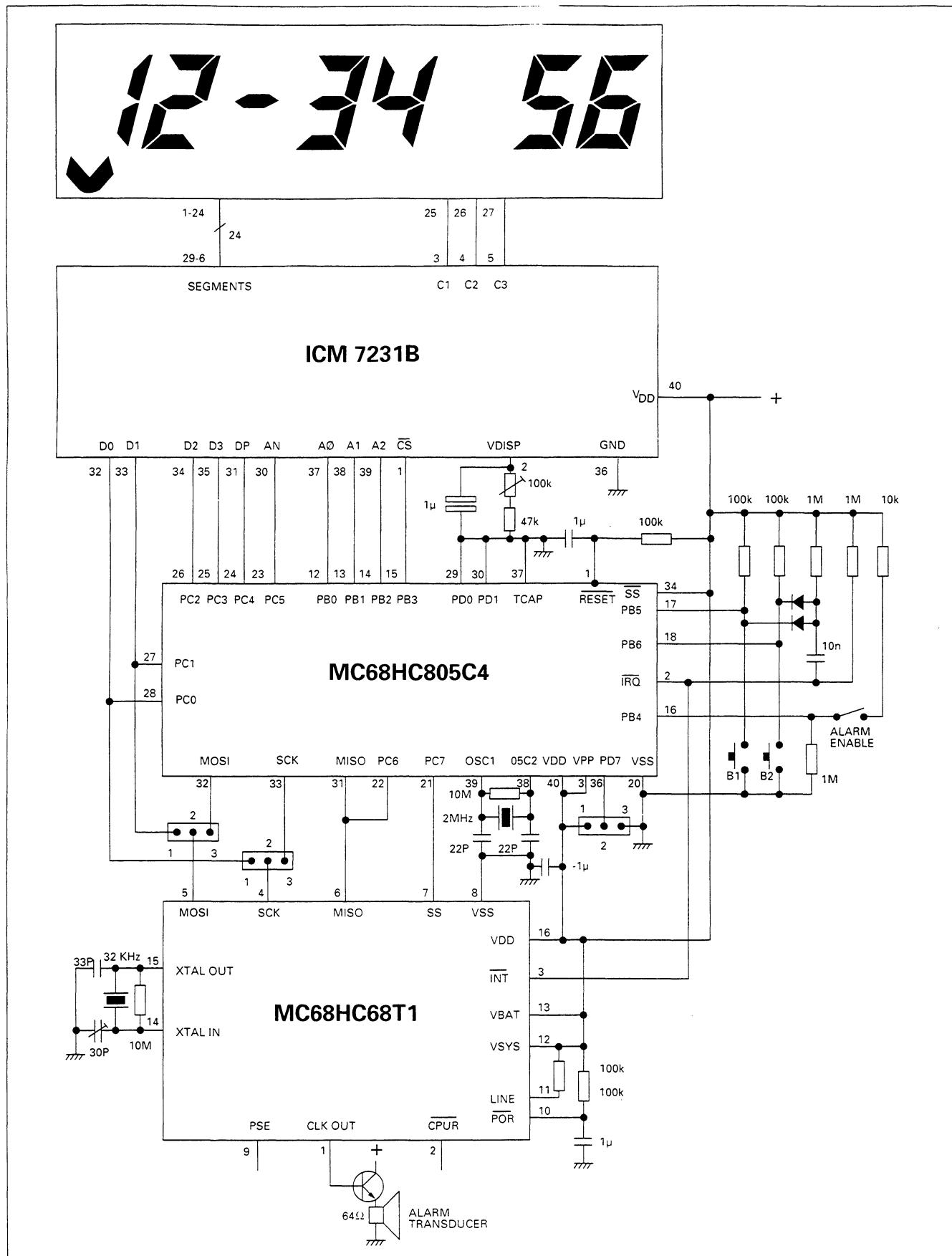


Figure 2.

```

1 ****
2 *
3 *      MC68HC68T1 - MC68HC05C4 Clock
4 *
5 *      P. Topping          16th March '89
6 *
7 ****
8
9 00000001      PORTB   EQU     $0001      PORT B DATA
10 00000002      PORTC   EQU     $0002      PORT C DATA
11 00000003      PORTD   EQU     $0003      PORT D DATA
12 00000005      DDRB    EQU     $0005      PORT B DDR
13 00000006      DDRC    EQU     $0006      PORT C DDR
14
15 0000000a      SPCR    EQU     $000A      SPI CONTROL REGISTER
16 0000000b      SPSR    EQU     $000B      SPI STATUS REGISTER
17 0000000c      SPD     EQU     $000C      SPI DATA REGISTER
18
19 00000000      SCK     EQU     0          SERIAL CLOCK
20 00000001      MOSI    EQU     1          "        OUT (MCU)
21 00000006      MISO    EQU     6          "        IN (MCU)
22 00000007      SS      EQU     7          "        ENABLE
23
24           ORG     $0050
25
26
27 00000050      STAT    RMB     1          STATUS FLAGS
28 *               0: MODE  0: NORM, 1: SET-UP
29 *               1: CHANGE 0: NO,   1: YES
30 *               2: REG.   0: TIME, 1: ALARM
31
32 00000051      Q       RMB     8          DISPLAY REGISTER
33 00000059      DSPLY   RMB     1          DISPLAY MODE
34 0000005a      W1      RMB     1          TEMP. STORAGE
35 0000005b      W2      RMB     1          "        "
36 0000005c      W3      RMB     1          "        "
37 0000005d      W4      RMB     1          "        "
38 0000005e      W5      RMB     1          "        "
39 0000005f      W6      RMB     1          "
40 00000060      ADDR    RMB     1          SERIAL ADDRESS
41 00000061      DPNT    RMB     1          RAM POINTER
42
43 00000062      SEC     RMB     1          68HC68T1 TIME SECONDS ($20)
44 00000063      MIN     RMB     1          "        " MINUTES ($21)
45 00000064      HOUR    RMB     1          "        " HOURS ($22)
46 00000065      DAY     RMB     1          "        " DAY ($23)
47 00000066      DATE    RMB     1          "        " DATE ($24)
48 00000067      MONTH   RMB     1          "        " MONTH ($25)
49 00000068      YEAR    RMB     1          "        " YEAR ($26)
50 00000069      ASEC    RMB     1          "        ALARM SECONDS ($28)
51 0000006a      AMIN    RMB     1          "        " MINUTES ($29)
52 0000006b      AHOUR   RMB     1          "        " HOURS ($2A)
53 0000006c      STREG   RMB     1          STATUS REGISTER ($30)
54 0000006d      CCREG   RMB     1          CLOCK CONTROL REGISTER ($31)
55 0000006e      ICREG   RMB     1          INT. CONTROL REGISTER ($32)
56
57           ORG     $0100

```

```

59 ****
60 *
61 *      Interrupts originating from the HC68T1      *
62 *      or a keypress start here.                      *
63 *
64 ****
65
66 00000100 0a0103    IRQ1   BRSET  5,PORTB,IRQ2
67 00000103 cc01ba    JMP    BUT1
68 00000106 0c0103    IRQ2   BRSET  6,PORTB,IRQ3
69 00000109 cc0212    JMP    BUT2
70
71 0000010c a601    IRQ3   LDA    #1      READ STATUS REGISTER
72 0000010e b75a    STA    W1      TO RESTORE IRQ TO HIGH
73 00000110 a630
74 00000112 b760
75 00000114 a66c
76 00000116 b761
77 00000118 cd02f6
78
79 0000011b 09010e    BRCLR  4,PORTB,NOTAL  ALARMS ARMED ?
80 0000011e 036c0b    BRCLR  1,STREG,NOTAL  YES, HAS ONE OCCURED ?
81 00000121 1e01    BSET   7,PORTB
82 00000123 a601
83 00000125 b75a
84 00000127 a6bf
85 00000129 cd02d4    LDA    #1
86
87 0000012c 3f5e    NOTAL  CLR    W5      READ ALL TIME REGISTERS
88 0000012e a607    LDA    #7
89 00000130 cd028d    JSR    RWSU
90 00000133 cd02f6    JSR    READ
91 00000136 ae08
92 00000138 045932    LDX    #8      PREPARE TO WRITE DISPLAY
93 0000013b a60a    BRSET  2,DSPLY,DISP2  NOT TIME ?
94 0000013d b753    DISP1  LDA    #$0A  TIME DISPLAY
95 0000013f b662
96 00000141 01590a    STA    Q+2
97 00000144 a401    BRCLR  0,DSPLY,SC  SECONDS
98 00000146 2704    AND    #$01
99 00000148 a60f
100 0000014a b753    BEQ    DT
101 0000014c b666    STA    #$0F
102 0000014e ad56    DT     STA    Q+2
103 00000150 b657    SC    DATE
104 00000152 2604    BSR    SPLIT
105 00000154 a60f
106 00000156 b757    LDA    Q+6
107 00000158 b663    CONT  LDA    MIN
108 0000015a ad4a    BSR    SPLIT
109 0000015c b664    LDA    HOUR
110 0000015e cd02e0    JSR    FORMAT
111 00000161 ad43    BSR    SPLIT
112 00000163 b651    LDA    Q
113 00000165 2637    BNE    OK
114 00000167 a60f    LDA    #$0F
115 00000169 b751    STA    Q
116 0000016b 2031    BRA    OK

```

```

118
119
120
121
122
123
124 0000016d 00591e          ****
125 00000170 b668             *
126 00000172 ad32             *      Date and alarm displays.
127 00000174 b667             *
128 00000176 ad2e             ****
129 00000178 b666             *
130 0000017a ad2a             *
131 0000017c a60a             *
132 0000017e b753             *
133 00000180 b756             *
134 00000182 b665             *
135 00000184 271c             *
136 00000186 e651             *
137 00000188 aa20             *
138 0000018a e751             *
139 0000018c 2014             *
140
141 0000018e b669             *
142 00000190 ad14             *
143 00000192 b66a             *
144 00000194 ad10             *
145 00000196 b66b             *
146 00000198 ad0c             *
147 0000019a a60a             *
148 0000019c b753             *
149 0000019e a60f             *
150 000001a0 b756             *
151 000001a2 cd02a1           *
152 000001a5 80               *
153
154 000001a6 b75b             *
155 000001a8 a40f             *
156 000001aa 5a               *
157 000001ab e751             *
158 000001ad b65b             *
159 000001af 44               *
160 000001b0 44               *
161 000001b1 44               *
162 000001b2 44               *
163 000001b3 a40f             *
164 000001b5 5a               *
165 000001b6 e751             *
166 000001b8 5a               *
167 000001b9 81               *

                                ****
DISP2   BRSET    0,DSPLY,DISP3  NOT DATE ?
        LDA      YEAR      DATE DISPLAY
        BSR      SPLIT
        LDA      MONTH     MONTH
        BSR      SPLIT
        LDA      DATE      DATE
        BSR      SPLIT
        LDA      #$OA     INSERT DASHES
        STA      Q+2
        STA      Q+5
        LDX      DAY       DAY OF WEEK
        BEQ      DISPJ
        LDA      Q,X      ZERO ?
        ORA      #$20     NO, GET DIGIT
        STA      Q,X      SET BIT FOR ANNUNCIATOR
        BRA      DISPJ
        PUT BACK

DISP3   LDA      ASEC     ALARM SECONDS
        BSR      SPLIT
        LDA      AMIN     ALARM MINUTES
        BSR      SPLIT
        LDA      AHOUR    ALARM HOURS
        BSR      SPLIT
        LDA      #$OA
        STA      Q+2
        OK      LDA      #$0F     BLANK SIXTH DIGIT
        STA      Q+5
        DISPJ  JSR      DISP
        RTI

SPLIT   STA      W2       EXTRACT 2 PACKED BCD
        AND      #$0F     DIGITS AND PUT INTO Q
        DECX
        STA      Q,X
        LDA      W2       LS BYTE
        LSRA
        LSRA
        LSRA
        AND      #$0F     MS BYTE
        DECX
        STA      Q,X
        DECX
        RTS


```

```

169 ****
170 *
171 *      Button 1 routines.
172 *
173 ****
174
175 000001ba cd0285          BUT1   JSR     DELAY      DEBOUNCE
176 000001bd 0b01fd          BRCLR  5,PORTB,*  STAY UNTIL KEY RELEASED
177 000001c0 015003          BRCLR  0,STAT,SK2 SETUP MODE
178 000001c3 cc01e9          JMP    SET       YES
179 000001c6 0f010c          BRCLR  7,PORTB,NOA ALARM ?
180 000001c9 1f01             BCLR   7,PORTB   YES, CANCEL IT
181 000001cb a601             LDA    #1
182 000001cd b75a             STA    W1
183 000001cf a6bc             LDA    #$BC
184 000001d1 cd02d4          JSR    CCONLY   CLOCK OUT LOW
185 000001d4 80               RTI
186
187 000001d5 3c59             NOA    INC     DSPLY     NO, MOVE TO NEXT
188 000001d7 b659             LDA    DSPLY     DISPLAY OPTION
189 000001d9 a106             CMP    #$06
190 000001db 2602             BNE    NOT6    TOO FAR ?
191 000001dd 3f59             CLR    DSPLY     YES, GO BACK TO ZERO
192 000001df cc010c          NOT6   JMP     IRQ3
193 ****
194 *
195 *
196 *      Button 1 setup function.
197 *
198 ****
199
200 000001e2 59592307311299  TABL1  FCB     $59,$59,$23,$07,$31,$12,$99
201
202 000001e9 1250             SET    BSET   1,STAT    CHANGE MADE
203 000001eb 3c5c             INC    W3
204 000001ed be5e             LDX    W5
205
206 000001ef d601e2          LDA    TABL1,X  MAXIMUM FOR REGISTER ?
207 000001f2 055003          BRCLR  2,STAT,NAL ALARM ?
208 000001f5 d601db          LDA    TABL1-7,X YES, OFFSET TABLE
209
210 000001f8 b15c             NAL    CMP    W3
211 000001fa 2402             BHS    NTB
212 000001fc 3f5c             CLR    W3
213
214 000001fe b65c             NTB    LDA    W3
215 00000200 a40f             AND    #$0F
216 00000202 a10a             CMP    #$0A
217 00000204 2508             BLO    NOADJ   MORE THAN 9 ?
218 00000206 a610             LDA    #$10
219 00000208 bb5c             ADD    W3
220 0000020a a4f0             AND    #$F0
221 0000020c b75c             STA    W3
222 0000020e b65c             LDA    W3
223 00000210 204b             BRA    DW5
                                         NOADJ
                                         DISPLAY NEW CONTENTS

```

```

225 ****
226 *
227 *      Button 2 routine (set-up).
228 *
229 ****
230
231 00000212 cd0285      BUT2   JSR     DELAY      DEBOUNCE
232 00000215 0d01fd      BRCLR  6,PORTB,*  STAY UNTIL KEY RELEASED
233 00000218 00500b      BRSET   0,STAT,REG  SETUP MODE ?
234 0000021b cd02c6      JSR     INTOFF    NO, STOP INTERRUPTS
235 0000021e 1050       BSET    0,STAT    AND GO TO SET-UP MODE
236 00000220 3f5e       CLR     W5        INITIALISE POINTER
237 00000222 3a5e       DEC     W5        FIRST INC GIVES ZERO
238 00000224 1350       BCLR   1,STAT    CHANGE FLAG RESET
239 00000226 03500b      REG    BRCLR   1,STAT,NOSTOR  STORING
240
241 00000229 be5e       LDX     W5        YES
242 0000022b b65c       LDA     W3
243 0000022d e762       STA     SEC,X    STORE NEW VALUE IN RAM
244 0000022f ad5a       BSR     RWSU1
245 00000231 cd02f2      JSR     WRITE    AND ALSO IN HC68T1
246
247 00000234 1350      NOSTOR BCLR   1,STAT    CHANGE FLAG RESET
248 00000236 3c5e       INC     W5        NEXT REGISTER
249 00000238 be5e       LDX     W5
250 0000023a a30a       CPX     #$0A    TOO FAR ?
251 0000023c 2741      BEQ     EXSET   YES, EXIT FROM SETUP
252
253 0000023e a307       CPX     #$07    TIME REGISTER ?
254 00000240 2504       BLO     U7
255 00000242 1450       BSET   2,STAT    NO, ALARM REGISTER
256 00000244 2007       BRA     NORD    WRITE ONLY
257
258 00000246 1550      U7     BCLR   2,STAT    YES, TIME
259 00000248 ad41       BSR     RWSU1   SO READ BYTE FROM RTC
260 0000024a cd02f6      JSR     READ
261
262 0000024d be5e      NORD   LDX     W5        FETCH RAM DATA
263 0000024f e662      LDA     SEC,X
264 00000251 b75c      STA     W3
265 00000253 055007      BRCLR  2,STAT,DW5  IF ALARM THEN
266 00000256 ad33       BSR     RWSU1   WRITE RAM DATA
267 00000258 cd02f2      JSR     WRITE   INTO 68HC68T1
268 0000025b b65c      LDA     W3

```

```

270
271
272
273
274
275
276 0000025d b75b      DW5    STA     W2
277 0000025f 44          LSRA
278 00000260 44          LSRA
279 00000261 44          LSRA
280 00000262 44          LSRA
281 00000263 b757      STA    Q+6      MS DIGIT
282 00000265 b65b      LDA    W2
283 00000267 a40f      AND    #$0F
284 00000269 b758      STA    Q+7      LS DIGIT
285 0000026b a60f      LDA    #$0F
286 0000026d b751      STA    Q      CLEAR
287 0000026f b752      STA    Q+1     REST
288 00000271 b753      STA    Q+2     OF
289 00000273 b754      STA    Q+3     DISPLAY
290 00000275 b756      STA    Q+5
291 00000277 be5e      LDX    W5      REGISTER
292 00000279 bf55      STX    Q+4      ADDRESS
293 0000027b cd02a1    JSR    DISP
294 0000027e 80          RTI
295
296 0000027f 1150      EXSET   BCLR   0,STAT      RETURN
297 00000281 cd02ca    JSR    INTON     TO NORMAL MODE
298 00000284 80          ALEN   RTI
299
300 00000285 aeef      DELAY   LDX    #$FF      DEBOUNCE DELAY
301 00000287 5a          AG     DECX
302 00000288 26fd      BNE    AG
303 0000028a 81          RTS
304
305
306
307
308
309
310
311
312 0000028b a601      RWSU1   LDA    #1
313 0000028d b75a      RWSU    STA    W1
314 0000028f a620      LDA    #32
315 00000291 bb5e      ADD    W5
316 00000293 a126      CMP    #$26      IF GREATER THAN $26
317 00000295 2301      BLS    NO6      INCREMENT TO SKIP
318 00000297 4c          INCA
319 00000298 b760      NO6    STA    ADDR
320 0000029a a662      LDA    #SEC
321 0000029c bb5e      ADD    W5
322 0000029e b761      STA    DPNT
323 000002a0 81          RTS

```

```

325
326
327
328
329
330
331 000002a1 b601      DISP    LDA     PORTB      CLEAR
332 000002a3 a4f0       AND     #$F0       LS BYTE OF PORTB
333 000002a5 b701       STA     PORTB      IE DIGIT ADDRESS = 0
334 000002a7 ae08       LDX     #8
335 000002a9 090102     BRCLR   4,PORTB,AGAIN  ALARM ?
336 000002ac 1a51       BSET    5,Q
337 000002ae e650       AGAIN   LDA     Q-1,X
338 000002b0 b702       STA     PORTC
339 000002b2 1701       BCLR   3,PORTB      LATCH
340 000002b4 1601       BSET    3,PORTB      DIGIT
341 000002b6 5a         DECX
342 000002b7 2704       BEQ    OUT      DONE ?
343 000002b9 3c01       INC    PORTB      NO, GOTO NEXT DIGIT
344 000002bb 20f1       BRA    AGAIN
345 000002bd b601       OUT    LDA     PORTB
346 000002bf a480       AND     #$80      DON'T CANCEL ALARM
347 000002c1 b701       STA     PORTB
348 000002c3 3f02       CLR    PORTC
349 000002c5 81         RTS
350
351
352
353
354
355
356
357 000002c6 a600       INTOFF  LDA     #00000000
358 000002c8 2002       BRA    DOIT
359 000002ca a61c       INTON   LDA     #00011100  SET-UP CLOCK CONTROL
360 000002cc b76e       DOIT    STA     ICREG    AND INTERRUPT CONTROL
361 000002ce a602       LDA     #2
362 000002d0 b75a       STA     W1
363 000002d2 a6bc       LDA     #10111100  ENABLE COUNTERS
364 000002d4 b76d       CCONLY  STA     CCREG   SELECT 32kHz CRYSTAL
365 000002d6 a631       LDA     #$31    CLOCK OUT DISABLED
366 000002d8 b760       STA     ADDR
367 000002da ae6d       LDX     #CCREG
368 000002dc bf61       STX     DPNT
369 000002de 2012       BRA    WRITE
370
371 000002e0 03590e     FORMAT  BRCLR  1,DSPLY,EXIT  12 HOUR DISPLAY MOD.
372 000002e3 2602       BNE    NOTMID MIDNIGHT ?
373 000002e5 a612       LDA     #$12    YES, MAKE IT 12
374 000002e7 a112       NOTMID CMP    #$12    NO, PM ?
375 000002e9 2306       BLS    EXIT
376 000002eb abee       ADD    #$EE    NO, DO NOTHING
377 000002ed 2902       BHCS   EXIT
378 000002ef a006       SUB    #$06    YES, SUBTRACT $12
379 000002f1 81         EXIT   RTS    USING HALF CARRY FOR
                                  DEC. ADJUST OF 8 & 9PM

```

```

381 ****
382 *
383 *       68HC68T1 serial routine (I/O PINS).
384 *
385 ****
386
387 000002f2 1e60      WRITE   BSET    7,ADDR      BIT 7 HIGH
388 000002f4 2002      BRA     SERT
389
390 000002f6 1f60      READ    BCLR    7,ADDR      BIT 7 LOW
391
392 000002f8 1e02      SERT    BSET    SS,PORTC    ENABLE HIGH (CPOL=0)
393
394 000002fa 0f0347      BRCLR   7,PORTD,SPI  SELECT SERIAL LINK
395
396 000002fd b660      PPSER   LDA     ADDR      SEND CHIP ADDRESS
397 000002ff ad2c      BSR     SHIFT      CHIP ADDRESS OUT
398 00000301 0e601b      BRSET   7,ADDR,WRBUS READ OR WRITE ?
399
400 00000304 be61      RDBUS   LDX     DPNT
401 00000306 a608      LDA     #8
402 00000308 b75d      STA     W4
403 0000030a 1002      LOOP2   BSET    SCK,PORTC  CLOCK HIGH
404 0000030c 0c0200      BRSET   MISO,PORTC,*+3 DATA LINE (RESULT IN CARRY)
405 0000030f 1102      BCLR   SCK,PORTC  CLOCK LOW
406 00000311 79        ROL    0,X
407 00000312 3a5d      DEC    W4
408 00000314 26f4      BNE    LOOP2
409 00000316 3c61      INC    DPNT
410 00000318 3a5a      DEC    W1
411 0000031a 26e8      BNE    RDBUS
412 0000031c 1f02      BCLR   SS,PORTC  ENABLE LOW
413 0000031e 81        RTS
414
415 0000031f be61      WRBUS   LDX     DPNT      DATA BUFFER POINTER
416 00000321 f6        LDA     0,X      DATA
417 00000322 ad09      BSR     SHIFT
418 00000324 3c61      INC    DPNT
419 00000326 3a5a      DEC    W1      NO. BYTES
420 00000328 26f5      BNE    WRBUS
421 0000032a 1f02      BCLR   SS,PORTC  ENABLE LOW
422 0000032c 81        RTS
423
424 0000032d ae08      SHIFT   LDX     #8      SHIFT OUT 8 BITS
425 0000032f 49        SHIFT1  ROLA
426 00000330 2404      BCC    SHIFT2      ZERO ?
427 00000332 1202      BSET   MOSI,PORTC  NO, DATA = 1
428 00000334 2004      BRA    SHIFT3
429 00000336 1302      SHIFT2 BCLR   MOSI,PORTC  DATA = 0
430 00000338 2000      BRA    SHIFT3      DELAY
431 0000033a 1002      SHIFT3 BSET   SCK,PORTC  CLOCK HIGH
432 0000033c 1102      BCLR   SCK,PORTC  CLOCK LOW
433 0000033e 1302      BCLR   MOSI,PORTC  DATA LOW
434 00000340 5a        DECX
435 00000341 26ec      BNE    SHIFT1
436 00000343 81        RTS

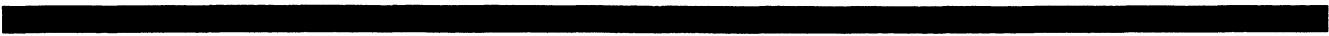
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438 ****
439 *
440 *      SPI serial routine. *
441 *
442 ****
443
444 00000344 b660          SPI    LDA     ADDR      GET ADDRESS
445 00000346 b70c          STA     SPD      SEND IT
446 00000348 0f0bfd        BRCLR  7,SPSR,*  WAIT TILL FINISHED
447 0000034b be61          LDX     DPNT     LOCATION OF 1st BYTE
448 0000034d f6            SPILP   LDA     0,X      GET IT
449 0000034e b70c          STA     SPD      SEND IT
450 00000350 0f0bfd        BRCLR  7,SPSR,*  WAIT TILL FINISHED
451 00000353 0e6003        BRSET   7,ADDR,SKIP ARE WE WRITING ?
452 00000356 b60c          LDA     SPD      NO, SO READ BYTE
453 00000358 f7            STA     0,X      AND SAVE IT
454 00000359 5c            SKIP    INCX    NEXT BYTE
455 0000035a 3a5a          DEC    W1       DECREMENT COUNT
456 0000035c 26ef          BNE    SPILP   FINISHED ?
457 0000035e 1f02          SPIP   BCLR    SS,PORTC YES, ENABLE LOW
458 00000360 81            RTS
459
460 ****
461 *
462 *      Reset routine. *
463 *
464 ****
465
466 00000361 3f01          RESET   CLR     PORTB
467 00000363 3f02          CLR     PORTC
468 00000365 a68f          LDA     #$8F    PORTB
469 00000367 b705          STA     DDRB    4, 5, & 6 INPUTS
470 00000369 a6bf          LDA     #$BF    PORTC
471 0000036b b706          STA     DDRC    ALL OUTPUTS EXCEPT 6
472 0000036d a655          LDA     #%-01010101  ENABLE SPI AS MASTER AT
473 0000036f b70a          STA     SPCR    500kHz, NO INTERRUPTS
474 00000371 cd02ca        JSR    INTON  INIT TO 1Hz INTERRUPTS
475 00000374 3f50          CLR    STAT
476 00000376 3f59          CLR    DSPLY
477 00000378 8e            STOPP  STOP
478 00000379 20fd          STOPP  BRA    STOPP
479
480 ****
481 *
482 *      MC68HC05C4 Vectors. *
483 *
484 ****
485
486          ORG    $1FF4
487 00001ff4 0361          FDB    RESET  SPI STATUS
488 00001ff6 0361          FDB    RESET  SCI STATUS
489 00001ff8 0361          FDB    RESET  TIMER
490 00001ffa 0100          FDB    IRQ1   EXTERNAL INTERRUPT
491 00001ffc 0361          FDB    RESET  SWI
492 00001ffe 0361          FDB    RESET  RESET
493
494          END

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